Abstract of the Disclosure

A semiconductor circuit system includes a first signal line and n circuit sections (n is an integer equal to or more than 2), each of which has an input terminal and an output terminal. The input terminals of predetermined k ones (k is an integer satisfying 2≤k<n) of the n circuit sections are connected to the first signal line, and the output terminal of a m-th one (1≤m≤n-k) of the n circuit sections is connected to the input terminal of a (m+k)-th one of the n circuit sections.